

INTELLEC
4-44



intel
intel
intel
intel
intel
intel
intel

INTEL CORPORATION
3065 Bowers Avenue
Santa Clara, California 95051
(408) 246-7501



November 5, 1974

Dear Customer:

This package contains your Intellec[®] 4/MOD 40 Microcomputer Development System. Before operating the system, please read the enclosed documentation and carefully follow all operating instructions. PLEASE FOLLOW THE TELETYPE INTERFACE INSTRUCTIONS IN THE HARDWARE REFERENCE MANUAL TO AVOID DAMAGING THE INTELLEC[®] MICROCOMPUTER DEVELOPMENT SYSTEM.

Please complete and return the enclosed system registration card as soon as possible to insure that you will receive all documentation updates.

We appreciate your interest in Intel microcomputers and hope to be of service to you in the development of your microcomputer systems.

Regards,

A handwritten signature in black ink, appearing to read "Mike Maerz".

Mike Maerz
Intellec[®] Product Manager
Microcomputer Systems

MM/esd

INTEL CORPORATION
3065 Bowers Avenue
Santa Clara, California 95051
(408) 246-7501



INTELLEC 4 MONITOR, VERSION 2.1

December 10, 1974

With this letter, we are distributing Version 2.1 of the Intellec 4 Monitor. Version 2.1 interfaces to the IMM4-90 Paper Tape Reader, as well as the standard ASR-33 Teletype. In addition, it allows the user to invert the contents of a section of memory, to program 1702A PROMs in either positive or complemented form. Additionally, it contains code to assure that check sums on tapes punched with the "W" command will always be correct.

Complete documentation on the Intellec 4 Monitor, Version 2.1, may be found in the November 1974 Intellec 4 Operator's Manual.

INTEL CORPORATION
3065 Bowers Avenue
Santa Clara, California 95051
(408) 246-7501



15 November 1974

INTELLEC 4 ASSEMBLER CONVERSION PROGRAM

Version 1.0

With the release of the new Intellec 4 Assembler, Version 2, we are distributing the Intellec 4 Assembler Conversion Program. This program can be used to convert source programs written for the old Intellec 4 Assembler into a form suitable for use with the new Intellec 4 Assembler. Complete instructions for the use of this program may be found in the enclosed External Reference Specification.

INTEL CORPORATION
3065 Bowers Avenue
Santa Clara, California 95051
(408) 246-7501



January 15, 1975

INTELLEC 4 ASSEMBLER, VERSION 3.0

The enclosed paper tape contains Version 3 of the Intellec 4 Assembler. This assembler accepts the source language described in the 4004/4040 Assembly Language Programming Manual, with the exception of the following constructs:

```
MACRO...ENDM
IF...ENDIF
operation codes imbedded in expressions
    (e.g., "FIM2, (LDM)")
TITLE
```

The number of symbols allowed in any one program varies with the size of the individual symbols and the amount of 4002 RAM (data RAM) present on the system. Each symbol requires one byte of memory for each of the first five characters of the symbol name, plus two bytes for the value of the symbol. An Intellec 4 with one bank of data RAM has 352 bytes of symbol table space, with each additional bank adding 128 more bytes. Thus, the number of symbols allowed varies from 50 (one data RAM bank, and all symbols five characters long) to 319 (eight data RAM banks and all symbols one or two characters long).

Additionally, the Intellec 4 Assembler now contains a paper tape editing capability, which allows the operator to insert or delete lines from an existing source program, and create a new source paper tape.

Complete instructions for using the Intellec 4 Assembler, including the Paper Tape Editing feature, may be found in the November 1974 edition of the Intellec 4 Operator's Manual.